DALLAS JUJX JU SEMICONDUCTOR JUZZINE Regulator

General Description

The DS2129 is a triple-output voltage regulator for 27-line, low-voltage-differential (LVD) SCSI termination for SPI-2 and SPI-3 applications using LVD termination networks. The device provides reference voltages and bias currents for LVD-termination resistor pi (π) networks. With a pi network (475 Ω , 121 Ω , 475 Ω), the DS2129 meets the common-mode bias, differential bias, and termination-impedance requirements of SPI-2 (Ultra2) and SPI-3 (Ultra3). The device also provides a 1.3V output for DIFFSENSE signaling, and includes protection features such as thermal shutdown and active current limiting.

Applications

SCSI Array Backplane SCSI Cables



Block Diagram

Features

- Fully Compliant with SPI-2 and SPI-3
- ♦ 2.7V to 5.25V Operation
- ♦ Differential Fail-Safe Bias

Ordering Information

		PIN-	TOP
FANI	VOLTAGE (V)	PACKAGE	MARK**
DS2129S	5	16 SO	DS2129
DS2129S+	5	16 SO	DS2129
DS2129S/T&R	5	16 SO	DS2129
DS2129S+T&R	5	16 SO	DS2129

+Denotes lead-free package.

*All devices rated for the commercial (0°C to +70°C) temperature range.

**A "+" anywhere on the topmark denotes a lead-free package.

Pin Configuration



BALLAS / XI/XI/VI

_ Maxim Integrated Products 1

For pricing, delivery, and ordering information, please contact Maxim/Dallas Direct! at 1-888-629-4642, or visit Maxim's website at www.maxim-ic.com.

ABSOLUTE MAXIMUM RATINGS

TERMPWR, DIFFSENSE, REG1, REG2	0.3V, +6.0V
REG1, REG2 Continuous Output Current	±200mA
Continuous Power Dissipation ($T_A = +70^{\circ}C$)	
16-Pin SO (derate 13mW/°C above +70°C)	1W

Operating Temperature Range	0°C to +70°C
Junction Temperature	+150°C
Storage Temperature Range	65°C to +150°C
Soldering Temperature	See IPC/JEDEC
	J-STD-020A Specification

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

(TERMPWR = 3.3V, $T_A = 0^{\circ}C$ to +70°C, unless otherwise noted.) (Note 1)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
TERMPWR						
TERMPWR Supply Current	ITERM	No load			40	mA
TERMPWR Voltage	VTERM		2.7		5.25	V
VREF REGULATOR			_			-
0.75V Regulator Output Voltage, REG2	V ₀₇₅	I _{OUT} = ±125mA	0.7	0.75	0.8	V
0.75V Regulator Source Current, REG2	ISRC_075	V _{OUT} = 0.25V			-200	mA
0.75V Regulator Sink Current, REG2	ISINK_075	V _{OUT} = 1.25V	+200			mA
0.75V Regulator Source Current Limit	ISRCL_075	V _{OUT} = 0V	-700			mA
0.75V Regulator Sink Current Limit	ISINKL_075	V _{OUT} = 3.3V			+700	mA
1.75V Regulator Output Voltage, REG1	V ₁₇₅	$I_{OUT} = \pm 125 \text{mA}$	1.7	1.75	1.8	V
1.75V Regulator Source Current, REG1	ISRC_175	V _{OUT} = 1.25V			-200	mA
1.75V Regulator Sink Current, REG1	ISINK_175	V _{OUT} = 2.25V	+200			mA
1.75V Regulator Source Current Limit	ISRCL_175	V _{REF} = 0V	-700			mA
1.75V Regulator Sink Current Limit	ISINKL_175	V _{REF} = 3.3V			+700	mA
DIFFSENSE OUTPUT						
DIFFSENSE Driver Output Voltage	VDSO	-5mA ≤ IDIFFSENSE ≤ 50µA	1.2	1.3	1.4	V
DIFFSENSE Driver Source Current	IDSH	VDIFFSENSE = 0V	-5		-15	mA
DIFFSENSE Driver Sink Current	IDSL	VDIFFSENSE = 2.4V	100		200	μA

ELECTRICAL CHARACTERISTICS (continued)

(TERMPWR = 3.3V, $T_A = 0^{\circ}C$ to +70°C, unless otherwise noted.) (Note 1)

PARAMETER	SYMBOL	CONDITIONS	MIN	ТҮР	MAX	UNITS
THERMAL SHUTDOWN						
Thermal-Shutdown Threshold (Note 2)		For increasing temperature		+150		°C
Thermal-Shutdown Hysteresis (Note 2)				10		°C

Note 1: All voltages are referenced to ground.

Note 2: Guaranteed by design and not production tested.



Typical Operating Circuit

DS2129

_Pin Description

PIN	NAME	FUNCTION
1, 8, 9, 10, 15, 16	N.C.	No Connection. Do not connect pins.
2	TERMPWR	Termination Power. Connect to SCSI TERMPWR line and decouple with a 4.7µF ceramic capacitor.
3	REG2	Regulator 2, 0.75V Output. Decouple with a 4.7μ F ceramic capacitor.
4	GND	Signal Ground
5, 11–14	HS_GND	Heat-Sink Ground. Internally connected to the mounting pad. Should be connected to ground.
6	REG1	Regulator 1, 1.75V Output. Decouple with a 4.7µF ceramic capacitor.
7	DIFFSENSE	DIFFSENSE Output. Drives the SCSI bus DIFFSENSE line.

Detailed Description

The DS2129 provides three regulated outputs. The REG1 provides a 1.75V output, and the REG2 provides a 0.75V output. Both regulators can sink and source 200mA of current. These regulators are current-limited to less than 700mA. The thermal-shutdown circuit disables the output buffers should the die temperature exceed +150°C. With a pi network of resistors (475 Ω , 121 Ω , 475 Ω) connected to these two outputs, the DS2129 provides common-mode bias, differential bias, and common impedance and differential impedance, as required in a terminator specified in SPI-2 and SPI-3 standards for a SCSI system. The third regulator provides a 1.3V output for DIFFSENSE signaling.

Chip Information

TRANSISTOR COUNT: 3778 CMOS and 80 BIPOLAR PROCESS: BICMOS SUBSTRATE CONNECTED TO GROUND

Thermal Information

Thermal Resistance (junction-to-ambient): $\theta_{JA} = +77^{\circ}C/W$ Thermal Resistance (junction-to-case): $\theta_{JC} = +25^{\circ}C/W$

Package Information

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information, go to <u>www.maxim-ic.com/packages</u>).



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